

Impedance Supply Inverter for Adjustable Speed Drives

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Abstract- This paper presents the simulation of mutual inductor primarily based impedance supply inverter (ZSI) for adjustable speed drives for stable operation by changing the hardware conciliation using discontinuous PWM strategy. The proposed inverter has an impedance network, this providing unique options that can't be obtained in traditional VSI and CSI. By controlling the shoot-through duty cycle ratio, the ZSI system provides ride through capability, reduces line harmonics, improves power factor, provides reliable operation and extends output voltage range. The ZSI using IGBT reduces the voltage stress across the switches. The analysis and simulation results will be presented to demonstrate these new options.

Index Terms: Mutual inductor, impedance source inverter, change speed drives

I. INTRODUCTION

The ancient general-purpose ASD system is predicated on VSI and CSI, which encompass diode rectifier, DC-link and Inverter Bridge. The figure.1 shows the VSI, that consists of capacitor as DC-link. As a result of of the VSI the ASD system suffers the following common limitations and issues.

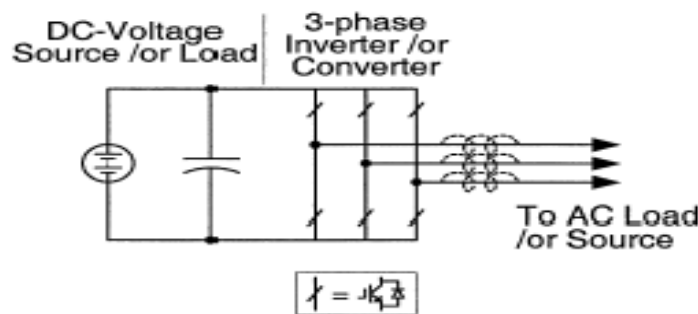
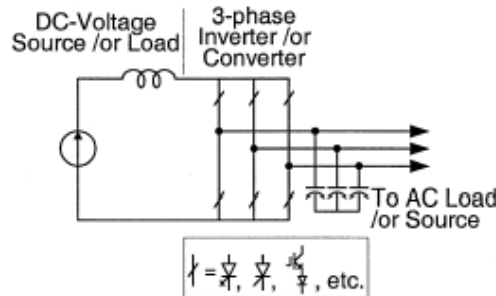


Figure-1. Voltage Source Inverter(VSI)

- The obtainable output voltage is limited quite below the input line voltage. Therefore the VSI is known as buck inverter.
- Inrush and the harmonic current from the diode rectifier can pollute the line. Low power factor is another issue of the traditional ASD system.

- Gating on the both devices in an exceedingly leg simultaneously, could leads to the shoot-through problem by EMI noises. This results in major killer to converters reliability.

The Fig.-2 shows the traditional three section CSI. The massive DC inductor provides the DC-link for CSI. The CSI has the following conceptual and theoretical barriers and limitations.



A. Figure 2 Current source inverter

- The AC output voltage has got to be larger than the first DC voltage that feeds the inductor.
- At least one in every of the upper devices and one in every of the lower devices must be gated on and maintained on at any time. otherwise an open circuit of DC inductor would occur and destroy the devices.

II. Z SOURCE INVERTER

The proposed (Impedance supply) inverter employs a unique impedance network (or circuit) to couple the inverter main circuit to the power supply, thus providing distinctive features that can't be obtained within the traditional voltage-source and current-source inverters where a capacitor and inductor are used, respectively. The Z-supply inverter overcomes the conceptual and theoretical barriers and limitations of the traditional voltage-source inverter and current-source inverter. By controlling the shoot-through duty cycle, the Z-supply will turn out any desired output ac voltage, even bigger than the line voltage. The Fig.-3 shows the block diagram of ZSI.

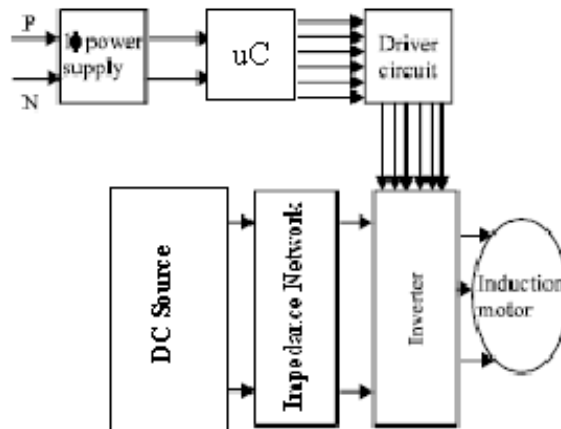


Figure 3. Block diagram of ZSI

The ZSI consist of rectifier unit, impedance network, Inverter Bridge, and gate driver circuits. The gating signals are generated from the micro controller unit.

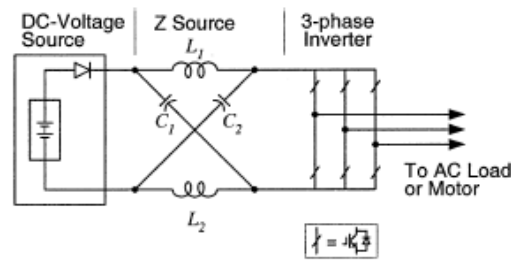


Figure-4. Impedance Source Inverter

The figure-4 shows the Z-source inverter. The impedance network consists of 2 equal inductors and capacitors connected in series and diagonal arms respectively.

The maximum constant boost control methodology is employed in this proposed inverter. This results in the reduction of volume and price by keeping the shoot duty ratio constant. The discontinuous PWM can minimize the harmonic content, this signal feed to IGBT gate terminal, depending upon the gating signal of the inverter operates.

III. PROPOSED SCHEME

The proposed theme extends the Z supply with one mutual inductor and one capacitor. The new network that is shown in fig.5 operates like the present principle with non-shoot through and shoot through states. Within the non shoot through state L1, L2 and C are connected in such a fashion as shown in Fig.5 and C is charged, while mutual inductor transfer energy from dc source to the main circuit. Within the shoot through state the inverter aspect is shorted by shorting any section leg. Throughout this state mutual inductor stores the energy and capacitor is discharged.

IV. SIMULATION

Simulations are performed to evolve the on top of analysis. The fig-5 shows the circuit configuration of proposed inverter and additionally shows the simulation waveform when the availability voltage $V=150V$ and therefore the impedance network parameters are $L1=L2=3mH$ and $C=1600\mu F$. The purpose of the system is to run the asynchronous drive for stable operation using renewable energy sources. The output voltage and current waveforms are shown in Figure. 6 (a)&(b) In this case the desired output for ac drive applications will be obtained results when $V_{dc}=150V$, $M=0.85$, $V_{out}=400V$.

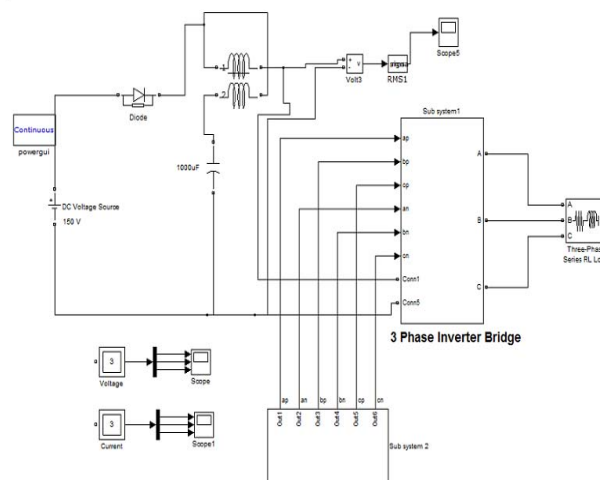


Fig. 5 Proposed circuit diagram of Mutual inductor z supply inverter

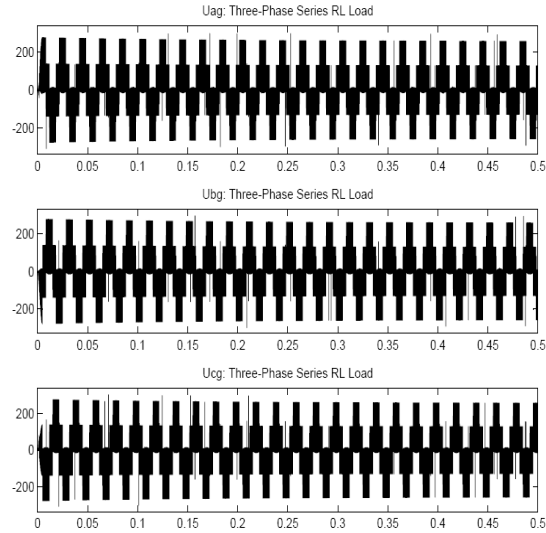


Figure.6(a) Voltage Waveform

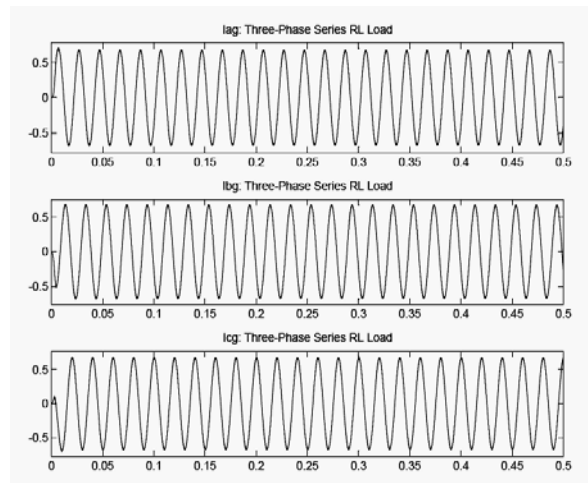


Figure.6 (b). Current Waveform

IV. CONCLUSION

This paper has presented a Z-supply power converter for implementing DC-AC & AC-AC power conversions. Simulation results verified the operation and demonstrated the promising features. In outline the mutual inductor based mostly ZSI system has several distinctive advantages that are very fascinating for several ASD applications. It will turn out any desired output voltage even greater than the rated voltage.

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